

REMARKS

Claims 1-24 are pending in the application. Claims 1, 8, and 10 have been rejected. Claims 2-7, 9, and 11-24 have been objected to. Applicants wish to thank the Examiner for the indication of allowability of claims 2-7, 9, and 11-24, if recast to no longer depend off a rejected claim. No new matter has been added.

Claim Rejections under 35 USC § 102

Claims 1 and 8 have been rejected under 35 U.S.C. 102(b) as being unpatentable in view of U.S. Patent No. 6,263,014 (Hereinafter "Chang"). Applicants respectfully traverse the rejection in view of the following arguments.

Summary of U.S. Patent No. 6,263,014 ("Chang")

The Chang reference is directed to a method of decoding a multi-level synchronous protocol having a first portion encoded as M/2-level signals and a subsequent portion encoded as M/2-level signal or M-level signals, wherein the M/2-level signals have expected larger than normal variation of deviations includes steps of decoding the first portion using a biased mode which uses M correlators shifted to adequately cover the frequency range of the expected larger than normal variation of deviations and determining from decoding of the first portion whether the subsequent portion is the M/2-level or M-level signal. If a M/2-level signal is found, continue decoding in the biased mode. If it's the M-level signal, then decoding continues in standard mode, which uses M correlators that are spaced in frequency to match M spectral deviations within a predetermined frequency range.

Claim 1

Applicants respectfully disagree with the Examiners assessment of what is shown in Figure 8 of the Chang reference. The Chang reference cited by the Examiner fails to disclose each and every element of claim 1. Specifically, the Chang reference fails to disclose a feedback circuit for providing a plurality of feedback signals based on an output of said receiver circuit to synchronize receipt of said data of said source synchronous signal.

The examiner has indicated that item 20 of Figure 8 is a receiver and items 30, 40, 10, 50, 300, and 770 of Figure 8 make up a feedback circuit. Accepting this construction, there is

not a plurality of feedback signals. The Examiner has identified only one signal (output 300, input to 10, 40, and 30). This is not the same as the plurality of feedback signals of the present invention. Indeed, there is no mention in Chang of providing a plurality of feedback signals based on an output of said receiver circuit to synchronize receipt of said data of said source synchronous signal. Accordingly, the Chang reference fails to disclose all the elements of the claimed invention.

In light of the above comments, applicants respectfully submit that claim 1 of the present invention is not anticipated by, and are therefore in condition for allowance over, Chang. Such action is kindly requested.

Claim 8

Claim 8 depends from claim 1 and as such incorporates each and every element of claim 1. For the reasons set forth above, Chang fails to disclose each and every element of claim 1. Specifically, Chang fails to disclose a feedback circuit for providing a plurality of feedback signals based on an output of said receiver circuit to synchronize receipt of said data of said source synchronous signal. As such, the Chang reference fails to disclose each and every element of claim 8.

In light of the above comments, applicants respectfully submit that claim 8 of the present invention is not anticipated by, and are therefore in condition for allowance over, Chang. Such action is kindly requested.

Claim Rejections under 35 USC § 103

Claim 10 has been rejected under 35 U.S.C. 103(a) as being unpatentable over Chang. Applicants respectfully traverse the rejection in view of the following arguments.

Claim 10 depends from claim 1 and as such incorporates each and every element of claim 1. For the reasons discussed above with regard to the 35 USC 102 rejection, Applicants respectfully submit that Chang neither teaches nor suggests each and every element of claim 1. Specifically, Chang fails to teach or suggest a feedback circuit for providing a plurality of feedback signals based on an output of said receiver circuit to synchronize receipt of said data of

said source synchronous signal. As such, Chang fails to teach or suggest each and every element of claim 10.

In light of the above comments, applicants respectfully submit that each and every element of claim 10 of the present invention are not taught or suggested by Chang, and therefore Claim 10 is in condition for allowance over the Chang. Such action is kindly requested.


CONCLUSION

In view of the above amendment, applicant believes the pending application is in condition for allowance.

Applicant believes no fee is due with this statement. However, if a fee is due, please charge our Deposit Account No. 12-0080, under Order No. SMQ-068 from which the undersigned is authorized to draw.

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Respectfully submitted,

By 
James M. McKenzie
Registration No.: 51, 146
LAHIVE & COCKFIELD, LLP
28 State Street
Boston, Massachusetts 02109
(617) 227-7400
(617) 742-4214 (Fax)
Attorney/Agent For Applicant